EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	EP1338948.pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/11 07:58
L2	8	"1338948".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/11 08:02
L3	2	"6119241".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/11 08:03
L4	9	"6119241".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/11 08:15

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = KWA
First Name = SEH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09968473	7020724	150	09/28/2001	ENHANCED POWER REDUCTION CAPABILITIES FOR STREAMING DIRECT MEMORY ACCESS ENGINE	KWA, SEH
10002333	Not Issued	61	11/14/2001	Computing system and method to select data packet	KWA, SEH
10882544	Not Issued	93	06/30/2004	DYNAMIC LANE, VOLTAGE AND FREQUENCY ADJUSTMENT FOR SERIAL INTERCONNECT	KWA, SEH
11173220	Not Issued	30	06/30/2005	Power management system for computing platform	KWA, SEH
11173784	Not Issued	30	06/30/2005	Various methods and apparatuses for power states in a controller	KWA, SEH
11329328	Not Issued	30	01/09/2006	Enhanced power reduction capabilities for streaming direct memory access engine	KWA, SEH
11509131	Not Issued	25	08/23/2006	Dynamic lane, voltage and frequency adjustment for serial interconnect	KWA, SEH
10629967	Not Issued	93	07/30/2003	INTERFERENCE MITIGATION BY ADJUSTMENT OF INTERCONNECT TRANSMISSION CHARACTERISTICS	KWA, SEH W.
10749855	Not Issued	71	12/30/2003	TRANSACTION-BASED POWER MANAGEMENT IN A COMPUTER SYSTEM	KWA, SEH W.
11166646	Not Issued	30	06/23/2005	Method and system for deterministic throttling for thermal management	KWA, SEH W.
11384681	Not Issued	25	03/20/2006	Providing a deterministic window for an idle state	KWA, SEH W.
10158654	Not Issued	161	05/30/2002	Integrated thermoelectric power generator and catalytic converter	KWAK, SEHOON
10205661	Not Issued	161	07/24/2002	Multi-phase suspension coolant	KWAK, SEHOON
10603476	Not Issued	71	06/25/2003	Battery thermal management with phase transition	KWAK, SEHOON
09128283	Not Issued	161	08/03/1998	COGNITION ORIENTED EMERGENT BEHAVIOR SYSTEM CONTROLLER	KWAK, SEHUNG DAVID

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
KWA	SEH	Search

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Inventor Name Search Result

Your Search was:

Last Name = SRITANYARATANA
First Name = SIRIPONG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07284899	Not Issued	169	12/15/1988	COMPUTER MEMORY HAVING ITS OUTPUT LINES SELECTED FOR CONNECTION TO A DATA BUS BY THE MEMORY ADDRESS	SRITANYARATANA, SIRIPONG
07329528	Not Issued	161	03/28/1989	COMPUTER MEMORY FOR MINIMIZING NUMBER OF MEMORY CHIPS USED AND MAXIMIZING THEIR UTILIZATION	SRITANYARATANA, SIRIPONG
09216013	6452610	150	12/16/1998	METHOD AND APPARATUS FOR DISPLAYING GRAPHICS BASED ON FRAME SELECTION INDICATORS	SRITANYARATANA, SIRIPONG
09410380	6490703	150	09/30/1999	BUS POWER SAVINGS USING SELECTIVE INVERSION IN AN ECC SYSTEM	SRITANYARATANA, SIRIPONG
09534193	6633987	150	03/24/2000	METHOD AND APPARATUS TO IMPLEMENT THE ACPI C3 STATE IN A RDRAM BASED SYSTEM	SRITANYARATANA, SIRIPONG
09675493	6775785	150	09/29/2000	METHOD AND APPARATUS FOR ACCESS TO RESOURCES NOT MAPPED TO AN AUTONOMOUS SUBSYSTEM IN A COMPUTER BASED SYSTEM WITHOUT INVOLVEMENT OF THE MAIN OPERATING SYSTEM	SRITANYARATANA, SIRIPONG
09746086	6735659	150	12/21/2000	METHOD AND APPARATUS FOR SERIAL COMMUNICATION WITH A CO-PROCESSOR	SRITANYARATANA, SIRIPONG
09751427	Not Issued	161	12/29/2000	Method and apparatus to permit a peripheral device to become the default system bus master	SRITANYARATANA, SIRIPONG
09752627	6748548	150	12/29/2000	COMPUTER PERIPHERAL DEVICE THAT REMAINS OPERABLE WHEN CENTRAL PROCESSOR OPERATIONS ARE SUSPENDED	SRITANYARATANA, SIRIPONG
10091328	Not Issued	71	03/05/2002	Partially integrating wireless components of processor-based systems	SRITANYARATANA, SIRIPONG
10236338	6732288	150	09/06/2002	BUS POWER SAVINGS USING	SRITANYARATANA,

				SELECTIVE INVERSION IN AN ECC SYSTEM	SIRIPONG
10331499	7119803	150	12/30/2002	METHOD, APPARATUS AND ARTICLE FOR DISPLAY UNIT POWER MANAGEMENT	SRITANYARATANA, SIRIPONG
10340020	6971034	150	01/09/2003	POWER/PERFORMANCE OPTIMIZED MEMORY CONTROLLER CONSIDERING PROCESSOR POWER STATES	SRITANYARATANA, SIRIPONG
10749855	Not Issued	71	12/30/2003	TRANSACTION-BASED POWER MANAGEMENT IN A COMPUTER SYSTEM	SRITANYARATANA, SIRIPONG
11074233	Not Issued	30	03/05/2005	System and method of coherent data transfer during processor idle states	SRITANYARATANA, SIRIPONG

Inventor Search Completed: No Records to Display.

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SRITANYARATANA	SIRIPONG	Search

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